

ASC/2

PROGRAM REFERENCE CARD

INTERSECTION _____

CONTROLLER NUMBER _____ ENTERED BY: _____ DATE _____

BOOT _____ VER. _____ MAIN _____ VER. _____ HELP _____ VER _____ CONFIG _____

1. CONFIGURATION SUBMENU

1. CONTROLLER SEQUENCE

PRIORITY	1	2	3	4	5	6	7	8	9	10	11	12
RING 1												
RING 2												
CG												

PHASE NUMBER	1	2	3	4	5	6	7	8	9	10	11	12
PHASES IN USE												
EXCLUSIVE PED												

3. PHASE TO LOAD SWITCH (MMU) ASSIGNMENT

LOAD SWITCH (MMU)	SIGNAL DRIVER GROUP	LOAD SWITCH (MMU)	SIGNAL DRIVER GROUP
CHANNEL	PH/OLAP PED	CHANNEL	PH/OLAP PED
1		9	
2		10	
3		11	
4		12	
5		13	
6		14	
7		15	
8		16	

4. SDLC OPTIONS/ENABLES (BIU Number)

	1	2	3	4	5	6	7	8
TERM & FACIL								
DETECTOR RACK								
TYPE 2 RUNS AS TYPE 1								
MMU DISABLE								
DIAGNOSTIC ENABLE (TEST FIXTURE)								
PEER TO PEER ENABLE								
PEER TO PEER ADDRESS:								
1)		2)		3)		4)		5)
6)		7)		8)		9)		10)

5. PORT2 CONFIGURATION

PORT2 PROTOCOL	
PORT2 ENABLE	
AB3418 ADDRESS	
AB3418 GROUP ADDRESS	
AB3418 RESPONSE DELAY	
AB3418 SINGLE FLAG ENABLE	
AB3418 DROP-OUT TIME	
AB3418 TOD SF SELECT	
DATA RATE (BPS)	
DATA, PARITY, STOP	

6. PORT3 CONFIGURATION

PORT 3 PROTOCOL	
PORT 3 ENABLE	
TELEMETRY ADDRESS	
SYSTEM DETECTOR 9-16 ADDRESS	
TELEMETRY RESPONSE DELAY	
AB3418 ADDRESS	
AB3418 GROUP ADDRESS	
AB3418 RESPONSE DELAY	
AB3418 SINGLE FLAG ENABLE	
AB3418 DROP-OUT TIME	
AB3418 TOD SF SELECT	
ADDITIONAL SCREEN(S)	
DUPLEX -- HALF OR FULL	
MODEM DATA RATE (BPS)	
DATA, PARITY, STOP	

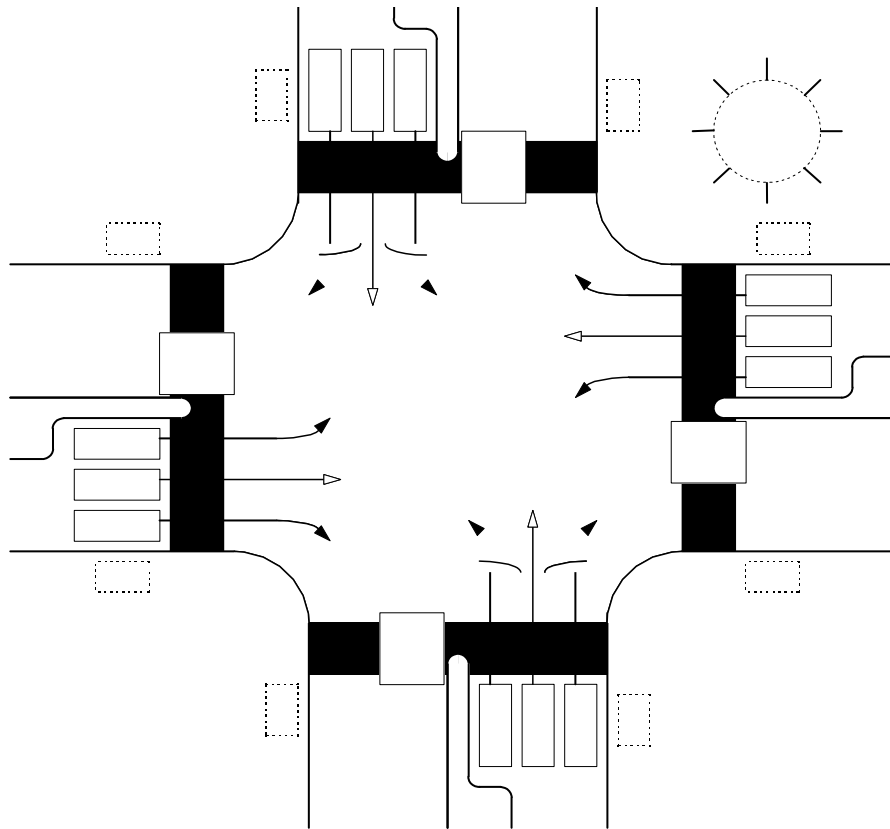
7. ENABLE EVENT LOGS

CRITICAL RFE'S (MMU/TF)	
NON-CRITICAL RFE'S (DET/TEST)	
DETECTOR ERRORS	
COORDINATION ERRORS	
MMU FLASH FAULTS	
LOCAL FLASH FAULTS	
PREEMPT	
POWER ON/OFF	
LOW BATTERY	
SPARE	
ALARM 1	
ALARM 2	
ALARM 3	
ALARM 4	
ALARM 5	
ALARM 6	
ALARM 7	
ALARM 8	
ALARM 9	
ALARM 10	
ALARM 11	
ALARM 12	
ALARM 13	
ALARM 14	
ALARM 15	
ALARM 16	

8. OPTIONS

SUPERVISOR ACCESS CODE	
DATA CHANGE ACCESS CODE	
KEY CLICK ENABLE	
BACKLIGHT ENABLE	

9. MMU PROGRAM

[illegible]

2. CONTROLLER SUBMENU

1. CONTROLLER TIMING DATA

[illegible]

2. PHASE OVERLAP ASSIGNMENTS

[illegible]

3. PED TIMING CARRYOVER

PHASE	CARRYOVER PHASE	PHASE	CARRYOVER PHASE
1		7	
2		8	
3		9	
4		10	
5		11	
6		12	

4. CONTROLLER RECALL DATA

[illegible]

5. CONTROLLER OVERLAP DATA

[illegible][illegible]

OVERLAP C	1	2	3	4	5	6	7	8	9	10	11	12
STANDARD												
PROTECTED												
PERMITTED												
ENABLE LAG												
ENABLE LEAD												
SPARE												
ADVANCE GREEN TIMER												
LAG/LEAD GREEN TIMER												
LAG/LEAD YELLOW TIMER												
LAG/LEAD RED TIMER												

[illegible][illegible]

6. CONTROLLER START/FLASH DATA

PHASE	1	2	3	4	5	6	7	8	9	10	11	12
POWER START												
EXTERNAL START												
ENTRY REM FLASH												
EXIT REM FLASH												
REM FLASH YEL												
FL TOGETHER PHS												
FL TOGETHER OVL PS	A:				B:				C:			D:
POWER START												
EXTERNAL START												
POWER START ALL RED ME												
POWER START FLASH ME												

REMOTE FLASH OPTIONS:												
OUT OF FLASH YELLOW												
OUT OF FLASH ALL RED												
MINIMUM RECALL												
SPARE												
FLASH THRU LOAD SWITCHES												
CYCLE THROUGH PHASES												

7. NO SERVE PHASES

CANNOT SERVE WITH												
PHASE	12	11	10	9	8	7	6	5	4	3	2	
1												
2												
3												
4												
5												
6												
7												
8												
9												
10												
11												

8. DIMMING

LOAD SWITCH	1	2	3	4	5	6	7	8
DIM GRN/WLK								
DIM YEL/PC								
DIM RED/DW								
LOAD SWITCH	9	10	11	12	13	14	15	16
DIM GRN/WLK								
DIM YEL/PC								
DIM RED/DW								

9. CONTROLLER OPTION DATA

PHASE	1	2	3	4	5	6	7	8	9	10	11	12
GUAR PASSAGE												
NONACTUATED I												
NONACTUATED II												
DUAL ENTRY												
COND SERVICE												
COND RESERVICE												
REST IN WALK												
FLASHING WALK												
FIVE SECTION LEFT TURN HEADS												
5-2				7-4					1-6			
3-8				11-10					9-12			

DUAL ENTRY		RESERVED	
COND SERVICE ENABLE		BACKUP PROTECTION GROUP 1	
COND SERVICE DET X SWITCHING		BACKUP PROTECTION GROUP 2	
PED CLR PROTECT		BACKUP PROTECTION GROUP 3	
SPEC PREEMPT OVLP FLASH		SIMULTANEOUS GAP GROUP 1	
LOCK DETECTORS IN RED ONLY		SIMULTANEOUS GAP GROUP 2	
RESERVED		SIMULTANEOUS GAP GROUP 3	

3. COORDINATOR SUBMENU

1. COORDINATOR OPTIONS

SPLIT UNITS		ACT CRD PHASE				
OFFSET UNITS		ACT WALK/REST				
INTERCNT FMT		INHIBIT MAX				
INTERCNT SRC		MAX2 SELECT				
RESYNC COUNT		MULTISYNC				
TRANSITION		FLOAT FORCE OFF				
DWELL PERIOD						
FREE ALTERNATE SEQUENCE	A	B	C	D	E	F

2. COORD MANUAL AND SPLIT DEMAND

[illegible]

3. COORD AUTO PERM MIN GREEN

PHASE	AUTO PERM MIN GRN	PHASE	AUTO PERM MIN GRN
1		7	
2		8	
3		9	
4		10	
5		11	
6		12	

4. PATTERN DATA STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN						
CYCLE LENGTH		OFFSTS	1		2		3	

SPLITS:													
PHASE 1)		2)			3)			4)					
PHASE 5)		6)			7)			8)					
PHASE 9)		10)			11)			12)					
VEH PERMISSIVE					[1]				[2]				
VEH PERM 2 DISP													
PHASE RESERVICE													
SPLIT EXTENSION/RING					[1]				[2]				
SPL DMD PATTERN					[1]				[2]				
XARTERY PATTERN													
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
COORD PHASES													
VEHICLE RECALL													
VEH MAX RECALL													
PED RECALL													
PHASE OMIT													
SPARE													
	A	B	C	D	E	F							
ALT SEQUENCE													

STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN						
CYCLE LENGTH		OFFSTS	1		2		3	

SPLITS:												
??		2)			3)			4)				
PHASE 5)		6)			7)			8)				
PHASE 9)		10)			11)			12)				
VEH PERMISSIVE					[1]				[2]			
VEH PERM 2 DISP												
PHASE RESERVICE												
SPLIT EXTENSION/RING					[1]				[2]			
SPL DMD PATTERN					[1]				[2]			
XARTERY PATTERN												
SPLITS:	1	2	3	4	5	6	7	8	9	10	11	12
COORD PHASES												
VEHICLE RECALL												
VEH MAX RECALL												
PED RECALL												
PHASE OMIT												
SPARE												
	A	B	C	D	E	F						
ALT SEQUENCE												

STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN						
CYCLE LENGTH		OFFSTS	1		2		3	

SPLITS:												
PHASE 1)		2)			3)			4)				
PHASE 5)		6)			7)			8)				
PHASE 9)		10)			11)			12)				
VEH PERMISSIVE					[1]			[2]				
VEH PERM 2 DISP												
PHASE RESERVICE												
SPLIT EXTENSION/RING					[1]			[2]				
SPL DMD PATTERN					[1]			[2]				
XARTERY PATTERN												
PHASE	1	2	3	4	5	6	7	8	9	10	11	12
COORD PHASES												
VEHICLE RECALL												
VEH MAX RECALL												
PED RECALL												
PHASE OMIT												
SPARE												
	A	B	C	D	E	F						
ALT SEQUENCE												

STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN	
CYCLE LENGTH		OFFSTS	1 2 3

SPLITS:													
PHASE 1)	2)		3)		4)								
PHASE 5)	6)		7)		8)								
PHASE 9)	10)		11)		12)								
VEH PERMISSIVE				[1]				[2]					
VEH PERM 2 DISP													
PHASE RESERVICE													
SPLIT EXTENSION/RING				[1]				[2]					
SPL DMD PATTERN				[1]				[2]					
XARTERY PATTERN													
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
COORD PHASES													
VEHICLE RECALL													
VEH MAX RECALL													
PED RECALL													
PHASE OMIT													
SPARE													
	A	B	C	D	E	F							
ALT SEQUENCE													

STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN	
CYCLE LENGTH		OFFSTS	1 2 3

SPLITS:													
PHASE 1)		2)			3)				4)				
PHASE 5)		6)			7)				8)				
PHASE 9)		10)			11)				12)				
VEH PERMISSIVE					[1]				[2]				
VEH PERM 2 DISP													
PHASE RESERVICE													
SPLIT EXTENSION/RING					[1]				[2]				
SPL DMD PATTERN					[1]				[2]				
XARTERY PATTERN													
PHASE		1	2	3	4	5	6	7	8	9	10	11	12
COORD PHASES													
VEHICLE RECALL													
VEH MAX RECALL													
PED RECALL													
PHASE OMIT													
SPARE													
		A	B	C	D	E	F						
ALT SEQUENCE													

STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN	
CYCLE LENGTH		OFFSTS	1 2 3

SPLITS:														
PHASE 1)	2)			3)			4)							
PHASE 5)	6)			7)			8)							
PHASE 9)	10)			11)			12)							
VEH PERMISSIVE				[1]				[2]						
VEH PERM 2 DISP														
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SPLIT EXTENSION/RING				[1]				[2]						
SPL DMD PATTERN				[1]				[2]						
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PHASE	1	2	3	4	5	6	7	8	9	10	11	12		
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VEHICLE RECALL														
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SPARE														
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COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN	
CYCLE LENGTH		OFFSTS	1 2 3

SPLITS:													
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PHASE 5)		6)			7)				8)				
PHASE 9)		10)			11)				12)				
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VEHICLE RECALL													
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PED RECALL													
PHASE OMIT													
SPARE													
		A	B	C	D	E	F						
ALT SEQUENCE													

STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN	
CYCLE LENGTH		OFFSTS	1 2 3

SPLITS:													
PHASE 1)	2)		3)		4)								
PHASE 5)	6)		7)		8)								
PHASE 9)	10)		11)		12)								
VEH PERMISSIVE				[1]				[2]					
VEH PERM 2 DISP													
PHASE RESERVICE													
SPLIT EXTENSION/RING				[1]				[2]					
SPL DMD PATTERN				[1]				[2]					
XARTERY PATTERN													
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
COORD PHASES													
VEHICLE RECALL													
VEH MAX RECALL													
PED RECALL													
PHASE OMIT													
SPARE													
	A	B	C	D	E	F							
ALT SEQUENCE													

STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN	
CYCLE LENGTH		OFFSTS	1 2 3

SPLITS:													
PHASE 1)		2)			3)				4)				
PHASE 5)		6)			7)				8)				
PHASE 9)		10)			11)				12)				
VEH PERMISSIVE					[1]				[2]				
VEH PERM 2 DISP													
PHASE RESERVICE													
SPLIT EXTENSION/RING					[1]				[2]				
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PHASE		1	2	3	4	5	6	7	8	9	10	11	12
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VEHICLE RECALL													
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PED RECALL													
PHASE OMIT													
SPARE													
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ALT SEQUENCE													

STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN	
CYCLE LENGTH		OFFSTS	1 2 3

SPLITS:														
PHASE 1)	2)			3)			4)							
PHASE 5)	6)			7)			8)							
PHASE 9)	10)			11)			12)							
VEH PERMISSIVE				[1]				[2]						
VEH PERM 2 DISP														
PHASE RESERVICE														
SPLIT EXTENSION/RING				[1]				[2]						
SPL DMD PATTERN				[1]				[2]						
XARTERY PATTERN														
PHASE	1	2	3	4	5	6	7	8	9	10	11	12		
COORD PHASES														
VEHICLE RECALL														
VEH MAX RECALL														
PED RECALL														
PHASE OMIT														
SPARE														
	A	B	C	D	E	F								
ALT SEQUENCE														

STD FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

PLAN FORMAT

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

TS2 FORMAT

COORD PATTERN		TIMING PLAN	
CYCLE LENGTH		OFFSTS	1 2 3

SPLITS:													
PHASE 1)		2)			3)				4)				
PHASE 5)		6)			7)				8)				
PHASE 9)		10)			11)				12)				
VEH PERMISSIVE					[1]				[2]				
VEH PERM 2 DISP													
PHASE RESERVICE													
SPLIT EXTENSION/RING					[1]				[2]				
SPL DMD PATTERN					[1]				[2]				
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PHASE		1	2	3	4	5	6	7	8	9	10	11	12
COORD PHASES													
VEHICLE RECALL													
VEH MAX RECALL													
PED RECALL													
PHASE OMIT													
SPARE													
		A	B	C	D	E	F						
ALT SEQUENCE													

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

COORD PATTERN		TIMING PLAN						
CYCLE LENGTH		OFFSTS	1		2		3	

SPLITS:													
PHASE 1)		2)			3)				4)				
PHASE 5)		6)			7)				8)				
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VEH PERMISSIVE					[1]				[2]				
VEH PERM 2 DISP													
PHASE RESERVICE													
SPLIT EXTENSION/RING					[1]				[2]				
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PHASE		1	2	3	4	5	6	7	8	9	10	11	12
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VEHICLE RECALL													
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PHASE OMIT													
SPARE													
		A	B	C	D	E	F						
ALT SEQUENCE													

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

COORD PATTERN		TIMING PLAN						
CYCLE LENGTH		OFFSTS	1		2		3	

SPLITS:													
PHASE 1)		2)			3)			4)					
PHASE 5)		6)			7)			8)					
PHASE 9)		10)			11)			12)					
VEH PERMISSIVE					[1]				[2]				
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SPLIT EXTENSION/RING					[1]				[2]				
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PHASE		1	2	3	4	5	6	7	8	9	10	11	12
COORD PHASES													
VEHICLE RECALL													
VEH MAX RECALL													
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PHASE OMIT													
SPARE													
		A	B	C	D	E	F						
ALT SEQUENCE													

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

COORD PATTERN		TIMING PLAN						
CYCLE LENGTH		OFFSTS	1		2		3	

SPLITS:													
PHASE 1)		2)			3)			4)					
PHASE 5)		6)			7)			8)					
PHASE 9)		10)			11)			12)					
VEH PERMISSIVE					[1]				[2]				
VEH PERM 2 DISP													
PHASE RESERVICE													
SPLIT EXTENSION/RING					[1]				[2]				
SPL DMD PATTERN					[1]				[2]				
XARTERY PATTERN													
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
COORD PHASES													
VEHICLE RECALL													
VEH MAX RECALL													
PED RECALL													
PHASE OMIT													
SPARE													
	A	B	C	D	E	F							
ALT SEQUENCE													

COORD PATTERN		OFFSET	
CYCLE LENGTH		C/O/S	

COORD PATTERN		OFFSET	
CYCLE LENGTH		PLAN	

COORD PATTERN		TIMING PLAN						
CYCLE LENGTH		OFFSTS	1		2		3	

SPLITS:													
PHASE 1)		2)			3)			4)					
PHASE 5)		6)			7)			8)					
PHASE 9)		10)			11)			12)					
VEH PERMISSIVE					[1]				[2]				
VEH PERM 2 DISP													
PHASE RESERVICE													
SPLIT EXTENSION/RING					[1]				[2]				
SPL DMD PATTERN					[1]				[2]				
XARTERY PATTERN													
PHASE		1	2	3	4	5	6	7	8	9	10	11	12
COORD PHASES													
VEHICLE RECALL													
VEH MAX RECALL													
PED RECALL													
PHASE OMIT													
SPARE													
		A	B	C	D	E	F						
ALT SEQUENCE													

4. PREEMPTOR SUBMENU

1. PRIORITY PREEMPTOR 1

[illegible]

2. PRIORITY PREEMPTOR 2

[illegible]

3. PRIORITY PREEMPTOR 3

[illegible]

4. PRIORITY PREEMPTOR 4

[illegible]

	1	2	3	4	5	6	7	8	9	10	11	12						
TERM PHASE OVLP																		
TRK CLR PHASE																		
HOLD PHASES																		
EXIT PHASES																		
EXIT CALLS																		
SPARE																		
TERM OVERLAP	A:			B:			C:			D:								
ACTIVE						PED DARK												
PRIORITY						PED ACTIVE												
DET LOCK						ZERO PC TIME												
HOLD FLASH						PC THRU YELLOW												
TERM OVLP ASAP						TERM PHASES												
DON'T OVERRIDE FLASH							ACTIVE ONLY DURING HOLD											
FLASH ALL OUTPUTS							NO CVM IN FLASH											
YELLOW-RED GOES GREEN							FAST FLASH GRN ON HOLD											
ENABLE MAX PREEMPT TIME							OUT OF FLASH											
MAX TIME							DURATION TIME											
MIN HOLD TIME							DELAY TIME											
MIN PED CLEAR							INHIBIT TIME											
EXIT MAX							HLD DELAY TIME											
	GRN						YEL		RED									
MINIMUM																		
TRACK CLEAR																		
HOLD																		
LINKED PREEMPTOR																		

[illegible][illegible]

5. NIC/TOD SUBMENU

1. NIC/TOD CLOCK/CALENDAR DATA

DATE SET:	
TIME SET:	
MANUAL NIC PROGRAM STEP	
MANUAL TOD PROGRAM STEP	
SYNC REFERENCE TIME	
SYNC REFERENCE	
WEEK 1 BEGINS ON 1ST SUNDAY	
DISABLE DAYLIGHT SAVINGS	
DST BEGINS LAST SUNDAY	

2. NIC/TOD WEEKLY PROGRAMS

WEEK	SUN	MON	TUE	WED	THU	FRI	SAT
1							
2							
3							
4							
5							
6							
7							
8							
9							
10							

3. NIC/TOD YEARLY PROGRAMS

WEEK OF YEAR	1	2	3	4	5	6	7	8
WEEKLY PROGRAM								
WEEK OF YEAR	9	10	11	12	13	14	15	16
WEEKLY PROGRAM								
WEEK OF YEAR	17	18	19	20	21	22	23	24
WEEKLY PROGRAM								
WEEK OF YEAR	25	26	27	28	29	30	31	32
WEEKLY PROGRAM								
WEEK OF YEAR	33	34	35	36	37	38	39	40
WEEKLY PROGRAM								
WEEK OF YEAR	41	42	43	44	45	46	47	48
WEEKLY PROGRAM								
WEEK OF YEAR				49	50	51	52	53
WEEKLY PROGRAM								

4. NIC/TOD HOLIDAY PROGRAM

HOLIDAY	FLOAT/FIXED	MON/MON	DOW/DOM	WOM/YEAR	PROG
1					
2					
3					
4					
5					
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34					
35					
36					

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TOD PROGRAM STEP													
DAY PGM NUM													
STEP BEGINS													
FLASH				DIM ENABLE									
RED REST				ALT VEH EXTSN									
SPARE 5				DET LOG ENABLE									
SPARE 3				SPARE 4									
TYPE 0 DELAY EN				SPARE 2									
DET DIAG PLAN													
ALTERNATE SEQUENCE		A		B		C		D		E		F	
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
MAX2 ENABLE													
MAX3 ENABLE													
VEH RECALL													
VEH MAX RECALL													
PED RECALL													
COND SERV INH													
PHASE OMIT													
SPECIAL FCTNS									(1 - 8)				

TOD PROGRAM STEP													
DAY PGM NUM													
STEP BEGINS													
FLASH				DIM ENABLE									
RED REST				ALT VEH EXTSN									
SPARE 5				DET LOG ENABLE									
SPARE 3				SPARE 4									
TYPE 0 DELAY EN				SPARE 2									
DET DIAG PLAN													
ALTERNATE SEQUENCE		A		B		C		D		E		F	
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
MAX2 ENABLE													
MAX3 ENABLE													
VEH RECALL													
VEH MAX RECALL													
PED RECALL													
COND SERV INH													
PHASE OMIT													
SPECIAL FCTNS									(1 - 8)				

TOD PROGRAM STEP													
DAY PGM NUM													
STEP BEGINS													
FLASH				DIM ENABLE									
RED REST				ALT VEH EXTSN									
SPARE 5				DET LOG ENABLE									
SPARE 3				SPARE 4									
TYPE 0 DELAY EN				SPARE 2									
DET DIAG PLAN													
ALTERNATE SEQUENCE		A		B		C		D		E		F	
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
MAX2 ENABLE													
MAX3 ENABLE													
VEH RECALL													
VEH MAX RECALL													
PED RECALL													
COND SERV INH													
PHASE OMIT													
SPECIAL FCTNS									(1 - 8)				

TOD PROGRAM STEP													
DAY PGM NUM													
STEP BEGINS													
FLASH				DIM ENABLE									
RED REST				ALT VEH EXTSN									
SPARE 5				DET LOG ENABLE									
SPARE 3				SPARE 4									
TYPE 0 DELAY EN				SPARE 2									
DET DIAG PLAN													
ALTERNATE SEQUENCE		A		B		C		D		E		F	
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
MAX2 ENABLE													
MAX3 ENABLE													
VEH RECALL													
VEH MAX RECALL													
PED RECALL													
COND SERV INH													
PHASE OMIT													
SPECIAL FCTNS									(1 - 8)				

TOD PROGRAM STEP													
DAY PGM NUM													
STEP BEGINS													
FLASH				DIM ENABLE									
RED REST				ALT VEH EXTSN									
SPARE 5				DET LOG ENABLE									
SPARE 3				SPARE 4									
TYPE 0 DELAY EN				SPARE 2									
DET DIAG PLAN													
ALTERNATE SEQUENCE		A		B		C		D		E		F	
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
MAX2 ENABLE													
MAX3 ENABLE													
VEH RECALL													
VEH MAX RECALL													
PED RECALL													
COND SERV INH													
PHASE OMIT													
SPECIAL FCTNS									(1 - 8)				

TOD PROGRAM STEP													
DAY PGM NUM													
STEP BEGINS													
FLASH				DIM ENABLE									
RED REST				ALT VEH EXTSN									
SPARE 5				DET LOG ENABLE									
SPARE 3				SPARE 4									
TYPE 0 DELAY EN				SPARE 2									
DET DIAG PLAN													
ALTERNATE SEQUENCE		A		B		C		D		E		F	
PHASE	1	2	3	4	5	6	7	8	9	10	11	12	
MAX2 ENABLE													
MAX3 ENABLE													
VEH RECALL													
VEH MAX RECALL													
PED RECALL													
COND SERV INH													
PHASE OMIT													
SPECIAL FCTNS									(1 - 8)				

6. DETECTORS

1. DETECTOR TYPE/TIMERS

DET	TYPE	LOCK	EXTEND	DELAY	NO RESET	LOG ENABLE
1						
2						
3						
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32						

2. DETECTOR PHASE ASSIGNMENT

DETECTOR	PHASE ASSIGNMENT											
	1	2	3	4	5	6	7	8	9	10	11	12
1												
2												
3												
4												
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6												
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31												
32												

3. PED AND SYSTEM DETECTOR LOCAL ASSIGNMENT

DETECTOR LOG INTERVAL					MINUTES	
LOCAL PED DET NUMBER	PHASE PED DETECTOR					
	1	2	3	4	5	6
	7	8	9	10	11	12
NUMBER						

LOCAL DETECTOR NUMBER	LOCAL SYSTEM DET NUMBER					
	1	2	3	4	5	6
	7	8	9	10	11	12
NUMBER						

[illegible]

SPEED DET NUMBER	1	2	3	4	5	6	7	8
ONE DETECTOR SPEED:								
LOCAL DET NUMBER								
VEHICLE LENGTH								
LOOP LENGTH								
TWO DETECTOR SPEED:								
LOCAL DET NUMBER								
SPEED TRAP LENGTH								
ENABLE LOG								
UNITS:								

[illegible][illegible]

8. DETECTOR DIAGNOSTIC INTERVAL

DETECTOR DIAGNOSTIC INTERVAL			
DIAGNOSTIC NUMBER	NO ACTIVITY	MAX PRESENCE	ERRATIC COUNTS
1			
2			
3			
4			
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